# **IOTUESDAY INTRODUCTION TO RENESAS SYNERGY**

19 APRIL 2016



# COMPANY PROFILE: RENESAS ELECTRONICS CORPORATION

- Operations started: 1st April, 2010Headquarters
  - Toyosu Foresia, Tokyo, Japan
  - President & CEO: Tetsuya Tsurumaru

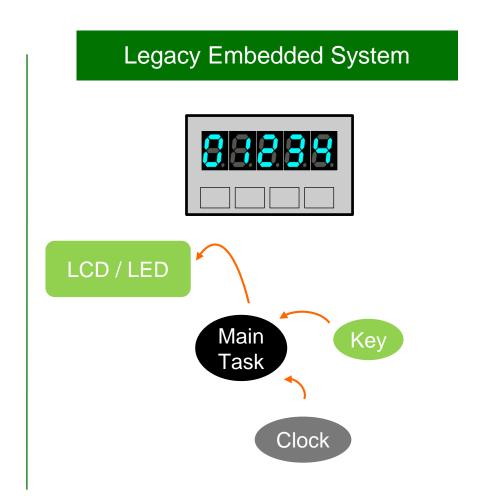
### **Number of Employees**

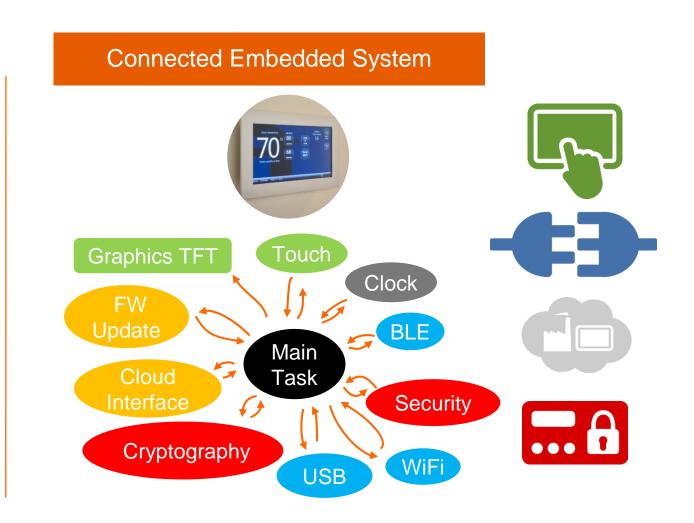
- 21,100 employees
   (consolidated basis as of Mar 2015)
- 6B\$ Revenue
- N°1 MCU supplier
- Major Operations
  - Research, development, design, manufacture, sale and servicing of semiconductor products





### **EMBEDDED SYSTEMS HAVE CHANGED**





### CHALLENGES WITH IOT & EMBEDDED SYSTEMS DEVELOPMENT



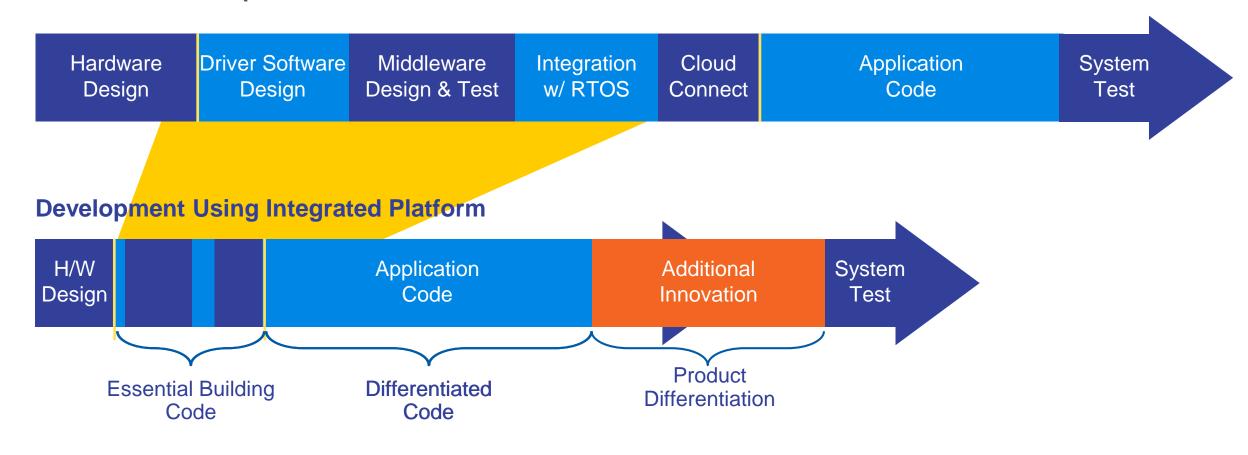


Synergy Platform will help your customers solve those challenges.

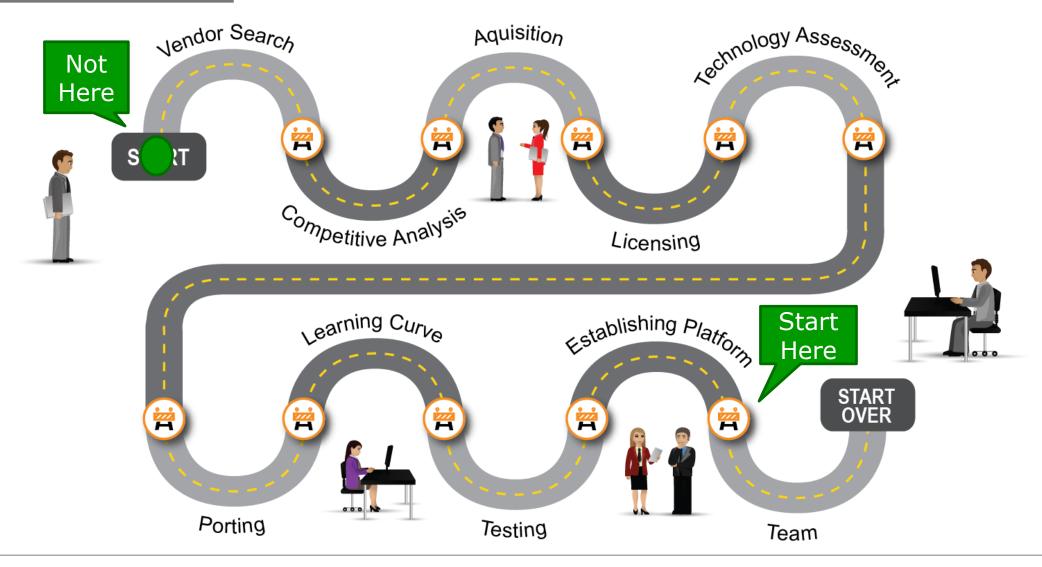


### DEVELOPERS NEED TO REDUCE DEVELOPMENT TIME

### **Traditional Development**

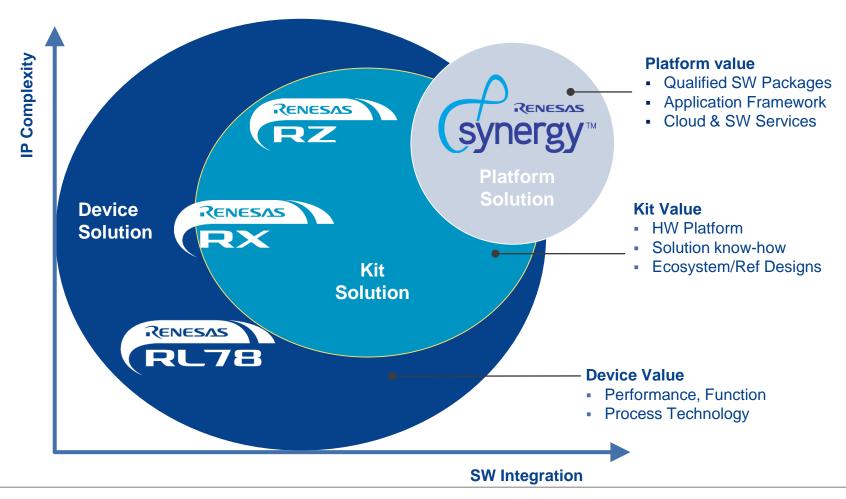


# DEVELOPERS NEED TO GET STARTED QUICKLY AND EASILY





### RENESAS MCU/MPU SOLUTIONS



# FIRST TO INTRODUCE AN IOT PLATFORM FOR EMBEDDED

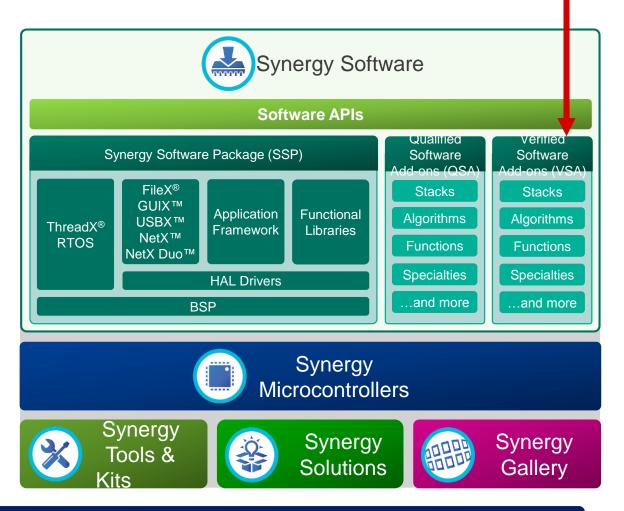


**DESIGNERS** 



A complete and qualified platform that **accelerates** embedded development, inspiring **innovation** and enabling **differentiation**.





The World's First Qualified Embedded MCU Software Platform.



# RENESAS SYNERGY™ SOFTWARE PLAT

hardware.

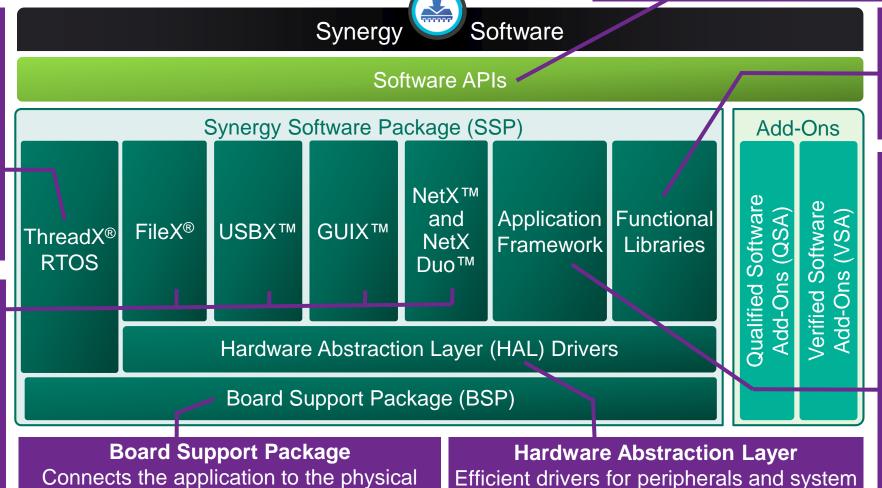
#### **Software APIs**

Standard interface to lower level components. Abstracts dependencies, ensures portability, and accelerates product development.

Redefining embedded development

ThreadX® RTOS
Coordinates,
schedules, and
monitors all tasks
within the
system.

Stacks & Middleware Complex capabilities modularized, optimized, and integrated.



Libraries

Specialized

software for DSP.

touch, security,

safety, and more.

**Application Framework** 

Links the RTOS

and middleware

to the HAL.

services.



### Synergy Software Package (SSP)

THEaux NTOS
Advanced Scheduler
Inter-process and Inter-thread Communication
Memory Management
Message Queue

ThreadX® RTOS

### FileX® FAT12/16/32, exFAT, SD, microSD, CF, and MemoryStick Complete Flash Management Very Fast Performance and Low Footprint

### USBX™ **Host Classes** (Storage, CDC, HID, Hub) Host Stack **Host Controller Device Classes** (Storage, CDC, Audio, HID, Printer) **Device Stack Device Controller**

GUIX ····	IN
Run Time Library	
Canvas	
Drawing	
Screen	
System	
Widget	
Window	

NetX™ and NetX™ Duo			
FTP	SNTP		
TFTP	NAT		
Telnet	TCP		
PPP	IPv4/v6		
SMTP	UDP		
POP3	ICMP		
HTTP	IGMP		
DNS	ARP		
DHCP	RARP		

Application Framework			
Audio	SPI		
Console	IIC		
JPEG	UART		
Touch Panel	Thread Monitor		
Messaging	ADC		
Cap. Touch Sensing	Power Profiles		
Block Media	External IRQ		

Application	Framework	Functional Libraries
Audio	SPI	Security &
Console	IIC	Encryption
JPEG	UART	CMSIS DSP
Touch Panel	Thread Monitor	
Messaging	ADC	
Cap. Touch Sensing	Power Profiles	
Block Media	External IRQ	

Hardware Abstraction Layer (HAL) Drivers							
Ethernet MAC	SDHI	ADC12	Low Power Capacitive Touch		Graphics LCD	Segment LCD	
Controller	QSPI	ADC14	Modes	es Sensing Unit		Controller	Controller
USBHS	SPI	DAC12	Clock r Management	Asynchronous General Purpose Timer		2D Drawing	Parallel Data
USBFS	IIC	DMA Controller				Engine	Capture Unit
CAN	SSI	Data Transfer	RTC	General PWM	WDT	JPEG Decoder	Security &
UART	Code Flash	Controller	Timer 32-Bit	Timer 32-Bit	VVD1	Safety	Encryption

Board Support Package (BSP)

Qualified Renesas Synergy Software package guarantees operation.



# IT'S A QUALIFIED PLATFORM - A COMMERCIAL GRADE PRODUCT

#### **Best Practices**

# Software Development Life Cycle (SDLC):

- Renesas SDLC guideline document
- Requirements & Traceability
- Coding Standards
- Design Descriptions
- Code Reviews and Unit Test Development
- Continuous Integration and Integration Reports
- Release Process & Management

# Software Data Sheet

# For Synergy Software Package (SSP) on multiple hardware platforms:

- Published and maintained on Renesas.com website
- Specs and performance metrics tested and documented
- Benchmarks, code size, context switch times, latencies, execution times, cyclical testing, fault tolerance and more.
- Basis of SSP warranty

### **Industry Standards**

# Well-respected standards for software development:

- MISRA C:2012 –
   Guidelines for the Use of the C Language in Critical Systems
- ISO/IEC/IEEE 12207 Software life cycle processes
- CERT 2<sup>nd</sup> Edition C Programming Language Secure Coding Standard
- Testing artifacts available for process certification -TUV, UL

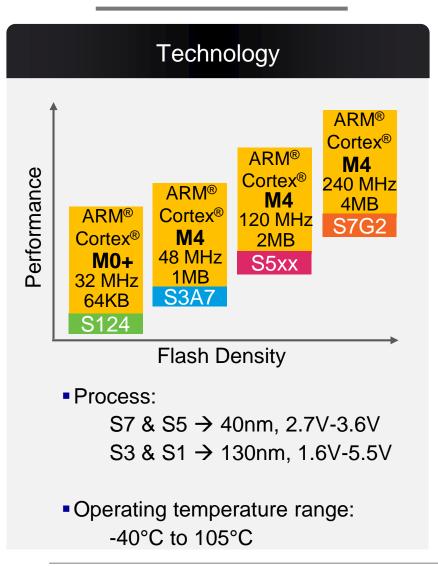
# **Software Quality Assurance (SQA)**

#### **Professional software:**

- Renesas SQA document
   Software Quality
   Assurance Plan
- Requirements traceability throughout development
- Documented processes
- SQA metrics & process artifacts available to customers
- Test plans, test suites, reports, software quality handbook



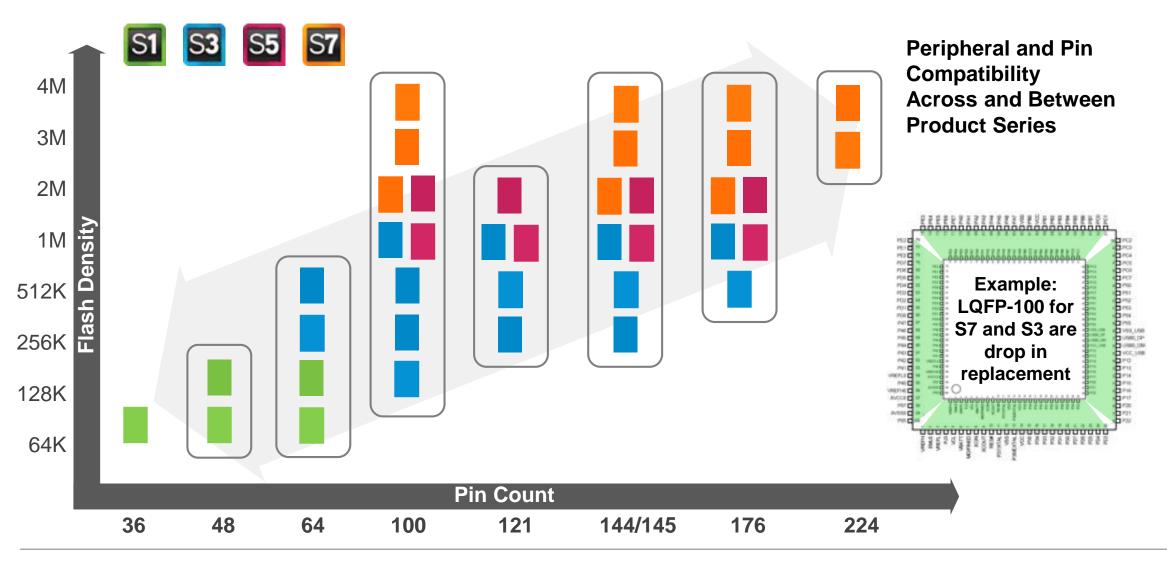
## RENESAS SYNERGY™ MICROCONTROLLERS





Four Microcontroller Series

# MICROCONTROLLER PORTFOLIO

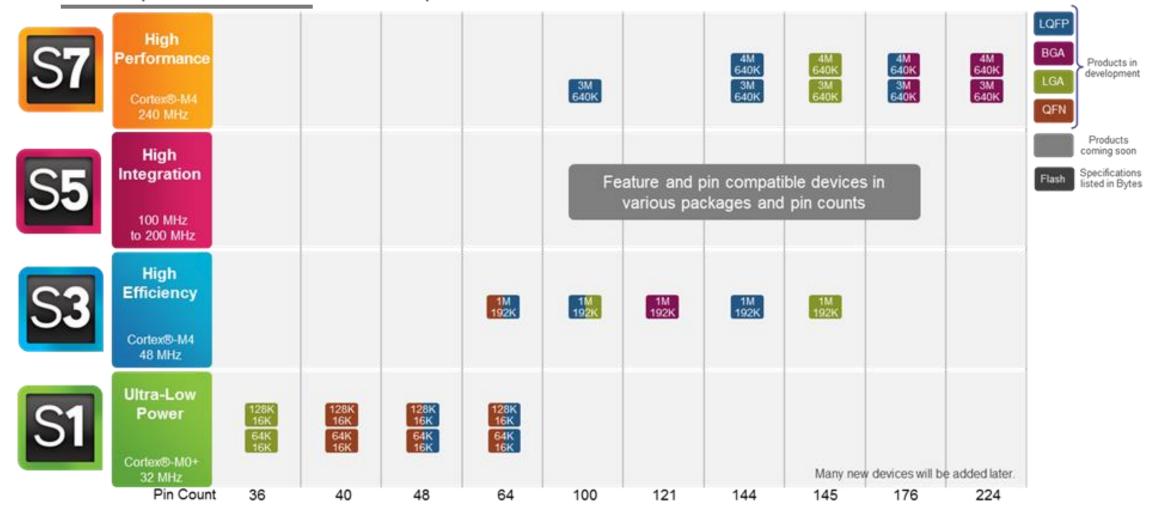


# Renesas Synergy™ MCU - Packages





Pin Compatible Products: Initial Lineup



THE THE SET SECTIONS AND ADDRESS OF THE PROPERTY OF THE PROPER



# **RENESAS SYNERGY™ KITS**







**Development Kit** 

**Starter Kit** 

**Promotion Kit** 

	DK	SK	PK
Purpose	Full Synergy Project Prototyping	Synergy Introduction and First Steps	Experience Synergy Platform
MCU Pin Access	All Pins	Most Pins	Some Pins
Expandability	Expansion & PMOD <sup>TM</sup> Connectors	Arduino <sup>™</sup> & PMOD <sup>™</sup> Connectors	Typically USB
SSP Qualification Basis	Yes	Yes	No
<b>BLE Connectivity</b>	Yes	Yes	Yes
On-board J-Link Debugger	Yes	Yes	Yes
Part Number	S7 DK-S7G2, S3 DK-S3A7, S1 DK-S124	<b>S7</b> SK-S7G2	S1 PK-S124

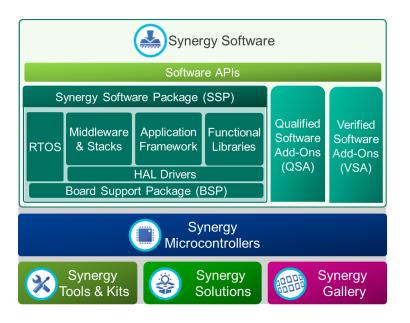
### **SUMMARY**

### Lowers TCO

Total cost of ownership

#### Scalable

A Platform that scales across the portfolio and to your needs



#### Efficient

 We've done the ground work so you can accelerate your development process and focus on inventing

### Qualified

Pre-integrated and tested software assures predictable behavior

